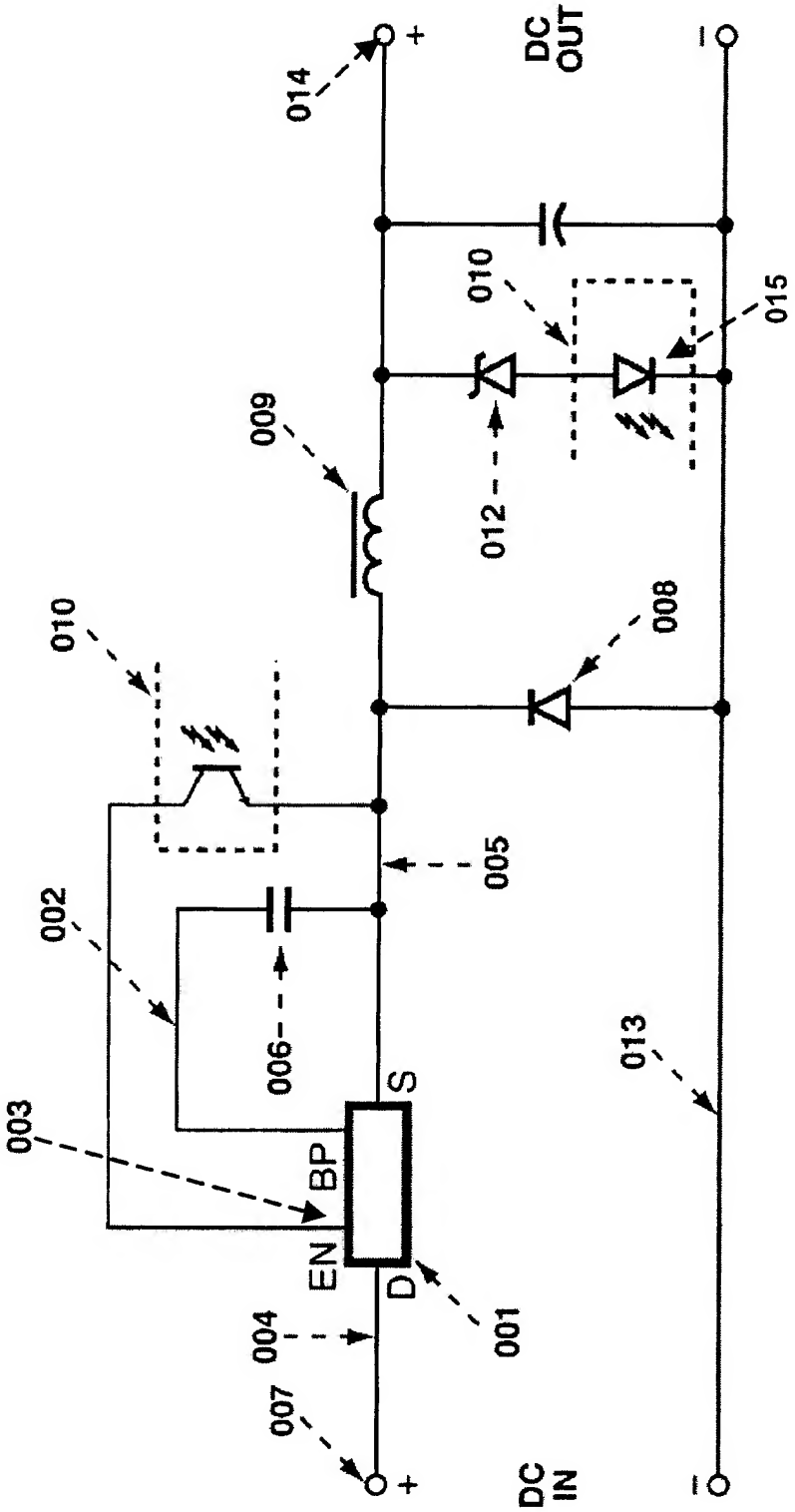
Figure 1


DC OUT
DC IN
+
-
014
010
015
009
012
008
010
005
002
006
003
S
BP
EN
D
004
001
007
013

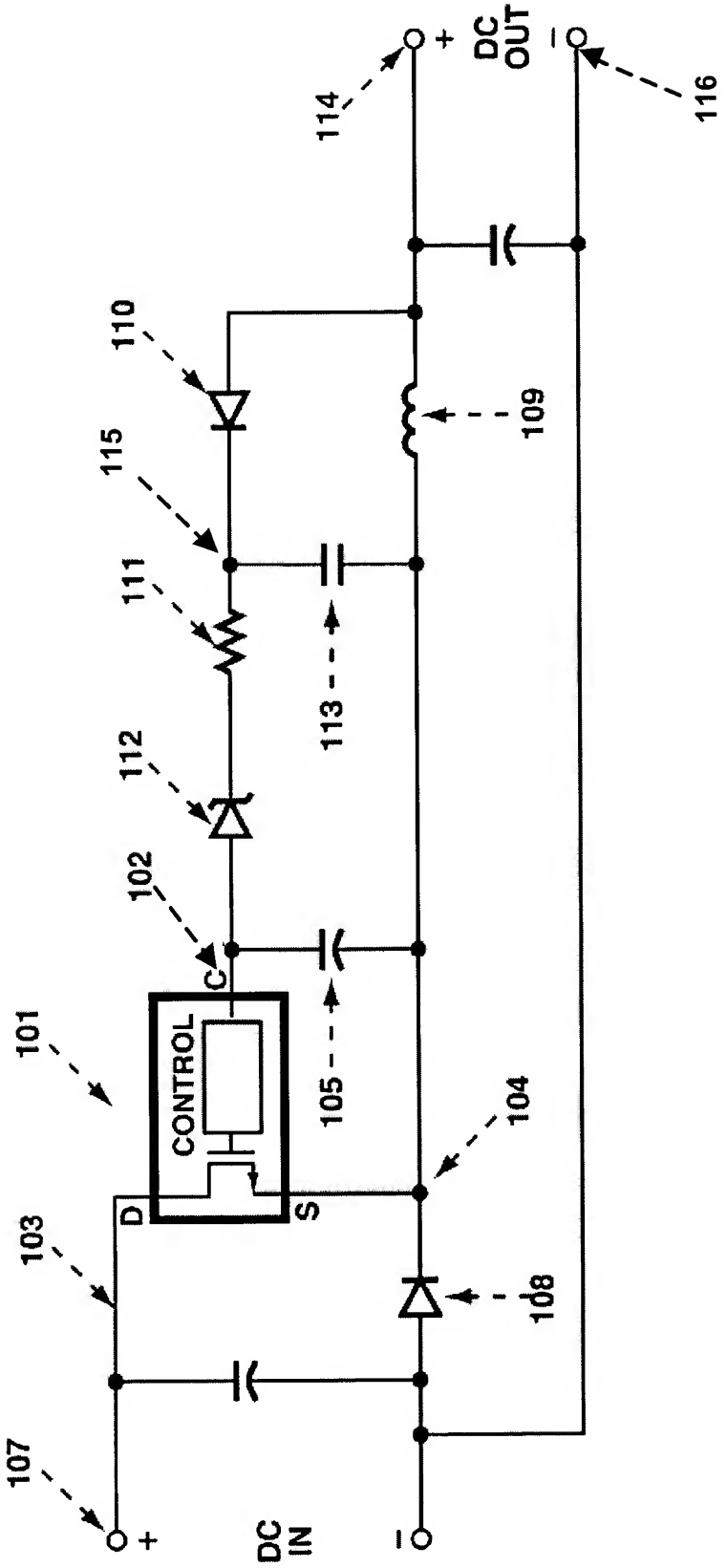
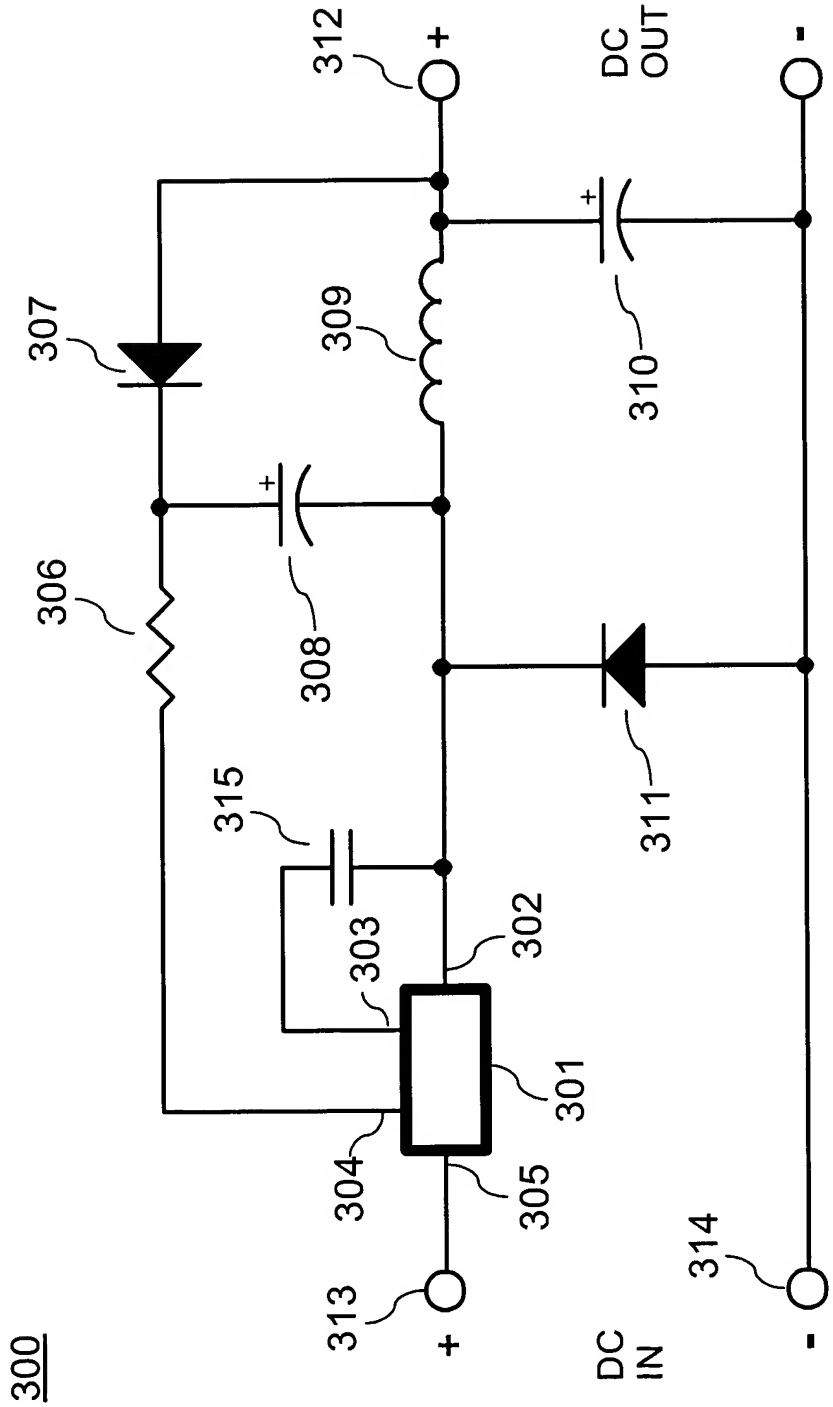

DC OUT
+
-
114
116
110
109
115
111
113
112
102
C
101
CONTROL
105
104
D
S
103
108
107
DC IN
+
-

Figure 2

Figure 3


300
313
+
DC IN
-
314
304
305
301
303
302
315
306
307
308
309
310
311
312
+
DC OUT
-

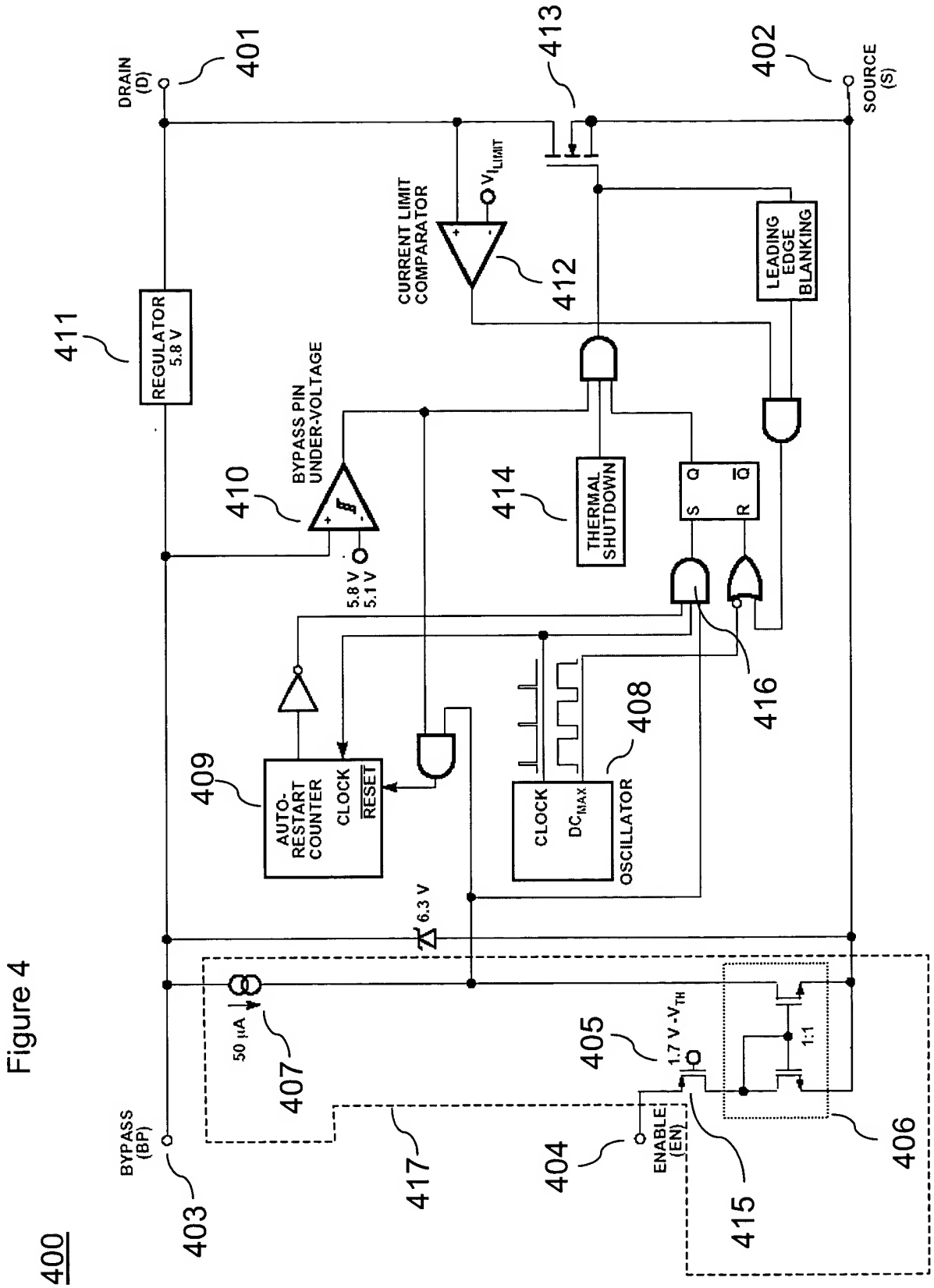

400
Figure 4
DRAIN (D)
401
SOURCE (S)
402
BYPASS (BP)
403
404
ENABLE (EN)
405
406
407
50 µA
408
409
410
411
412
413
414
415
416
417
REGULATOR 5.8 V
BYPASS PIN UNDER-VOLTAGE
5.8 V
5.1 V
CURRENT LIMIT COMPARATOR
$V_{I_{LIMIT}}$
THERMAL SHUTDOWN
LEADING EDGE BLANKING
S
Q
R
$\overline{Q}$
AUTO-RESTART COUNTER
CLOCK
RESET
CLOCK
DC_{MAX}
OSCILLATOR
6.3 V
1.7 V - V_{TH}
1:1

Figure 5

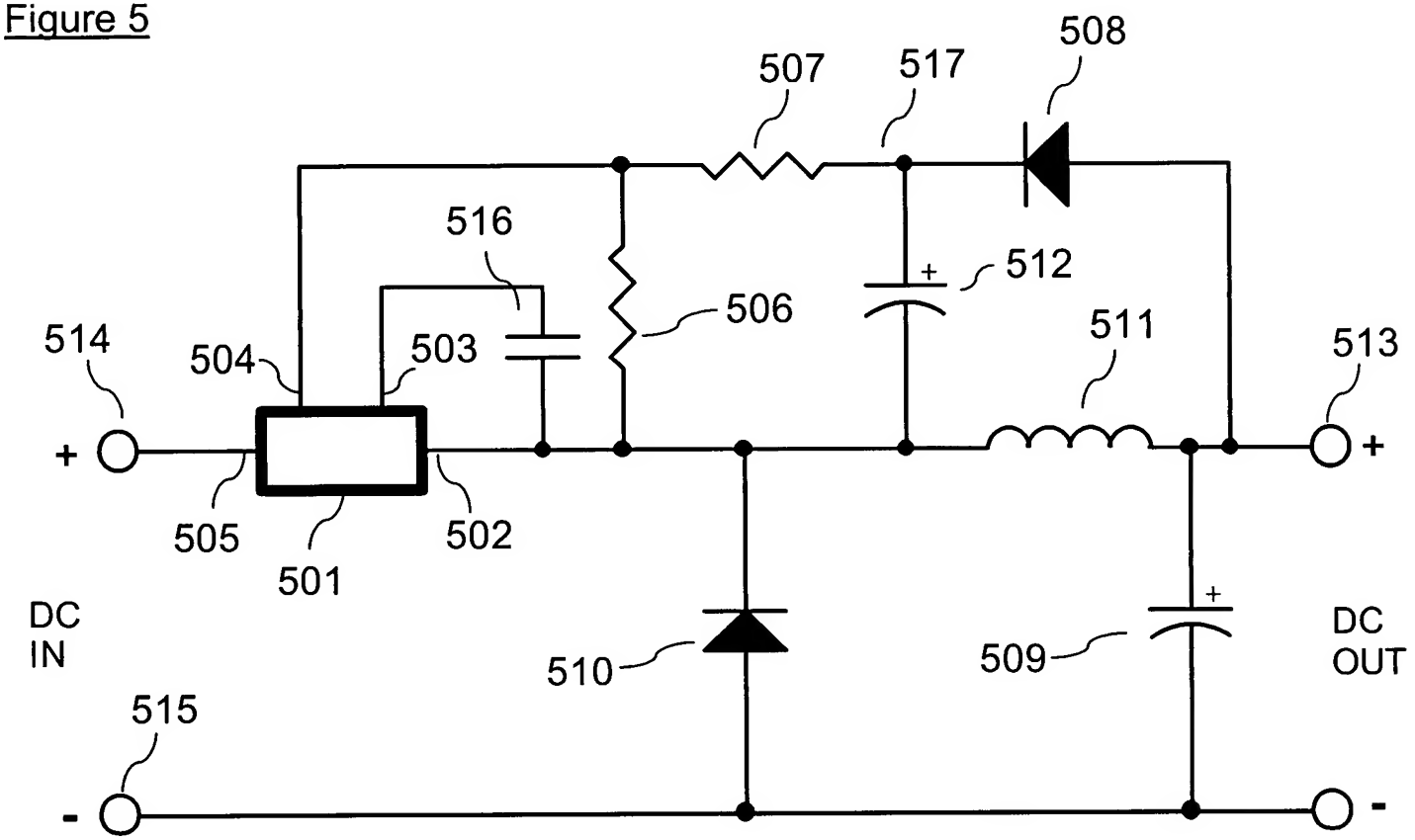

507
517
508
516
506
512
514
504
503
511
513
+
505
501
502
+
DC
IN
510
509
DC
OUT
515
-
-

Figure 6

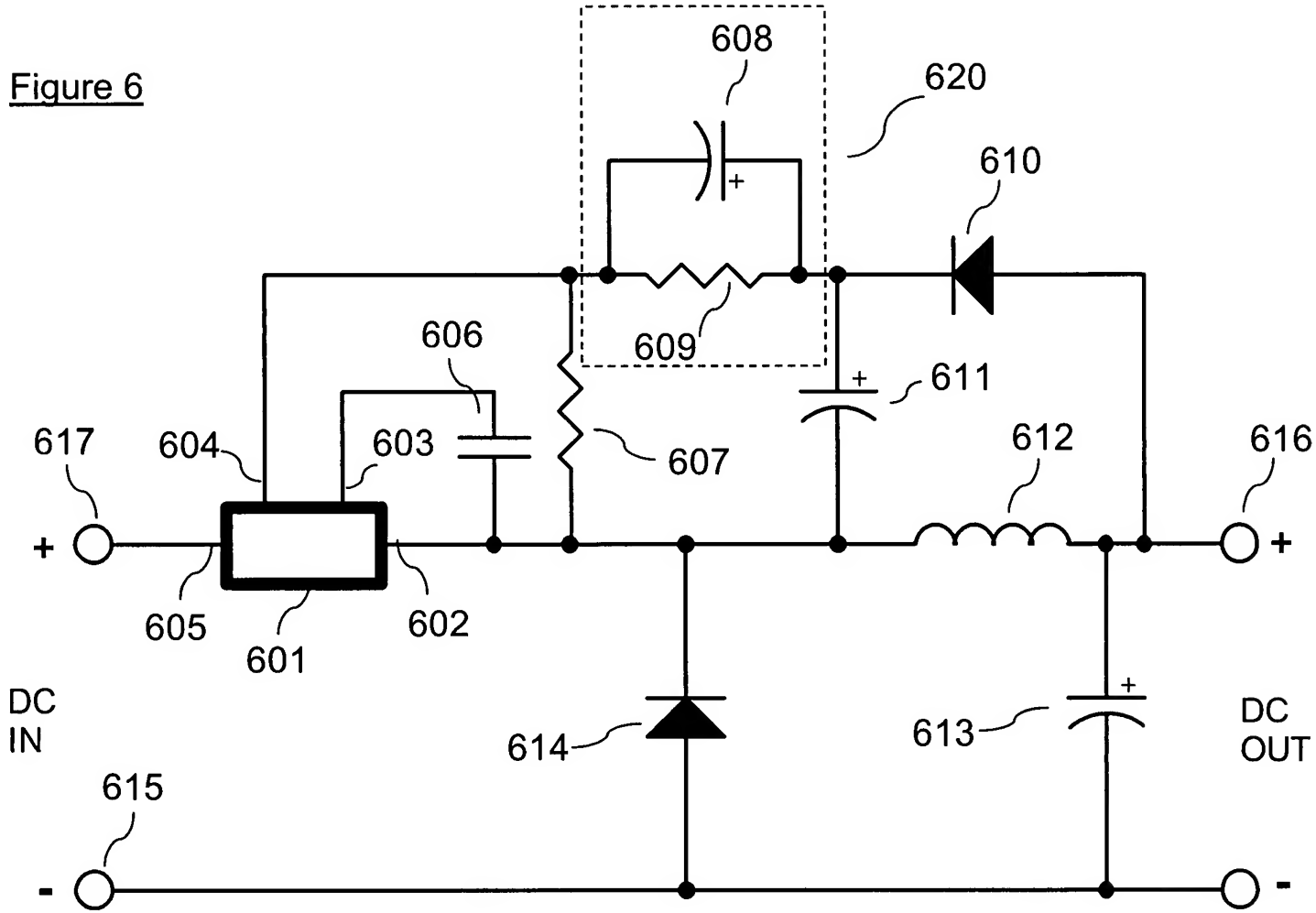

608
620
610
609
606
611
617
604
603
607
612
616
+
+
605
601
602
DC
IN
614
613
DC
OUT
615
-
-

Figure 7

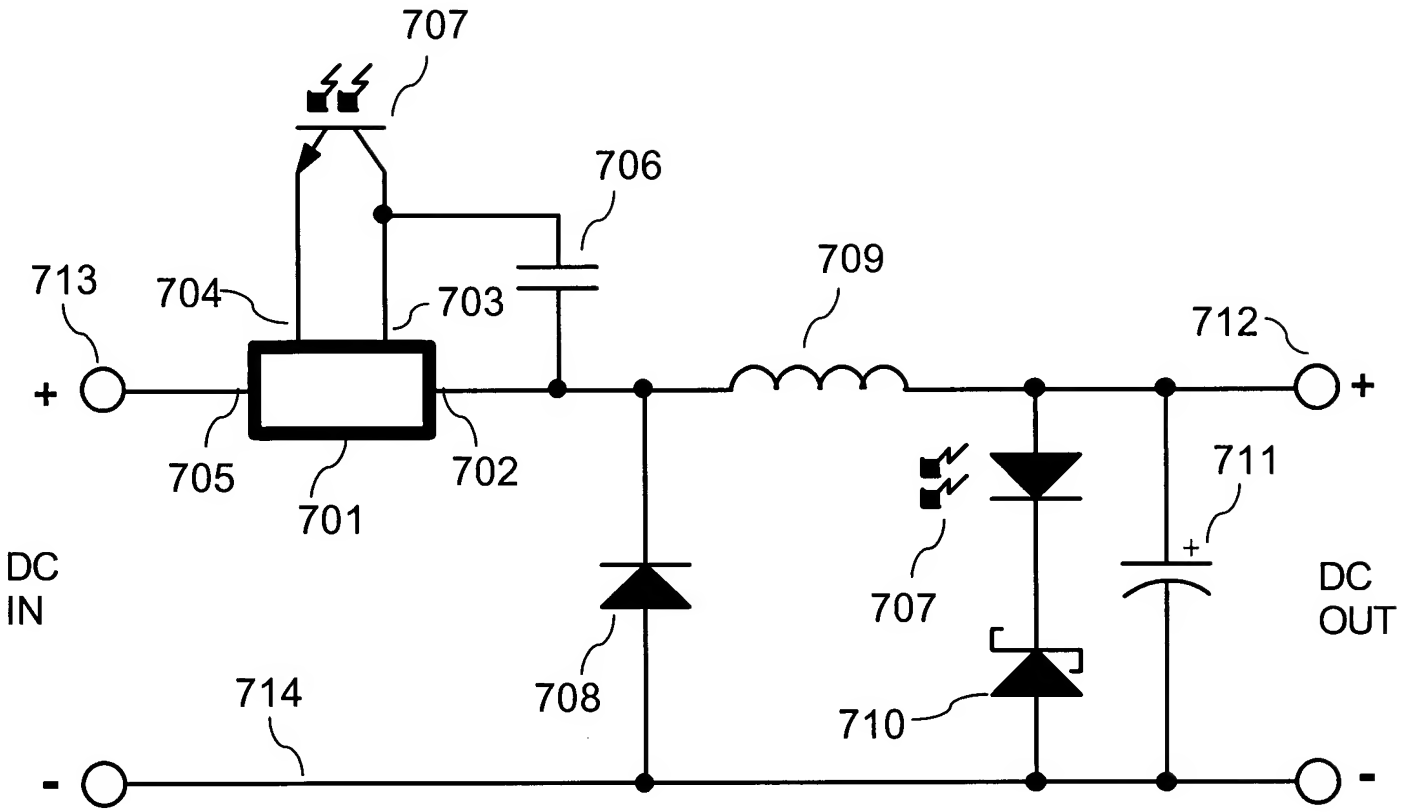

707
706
713
704
703
709
712
+
+
705
701
702
711
DC
IN
707
DC
OUT
714
708
710
-
-

Figure 8

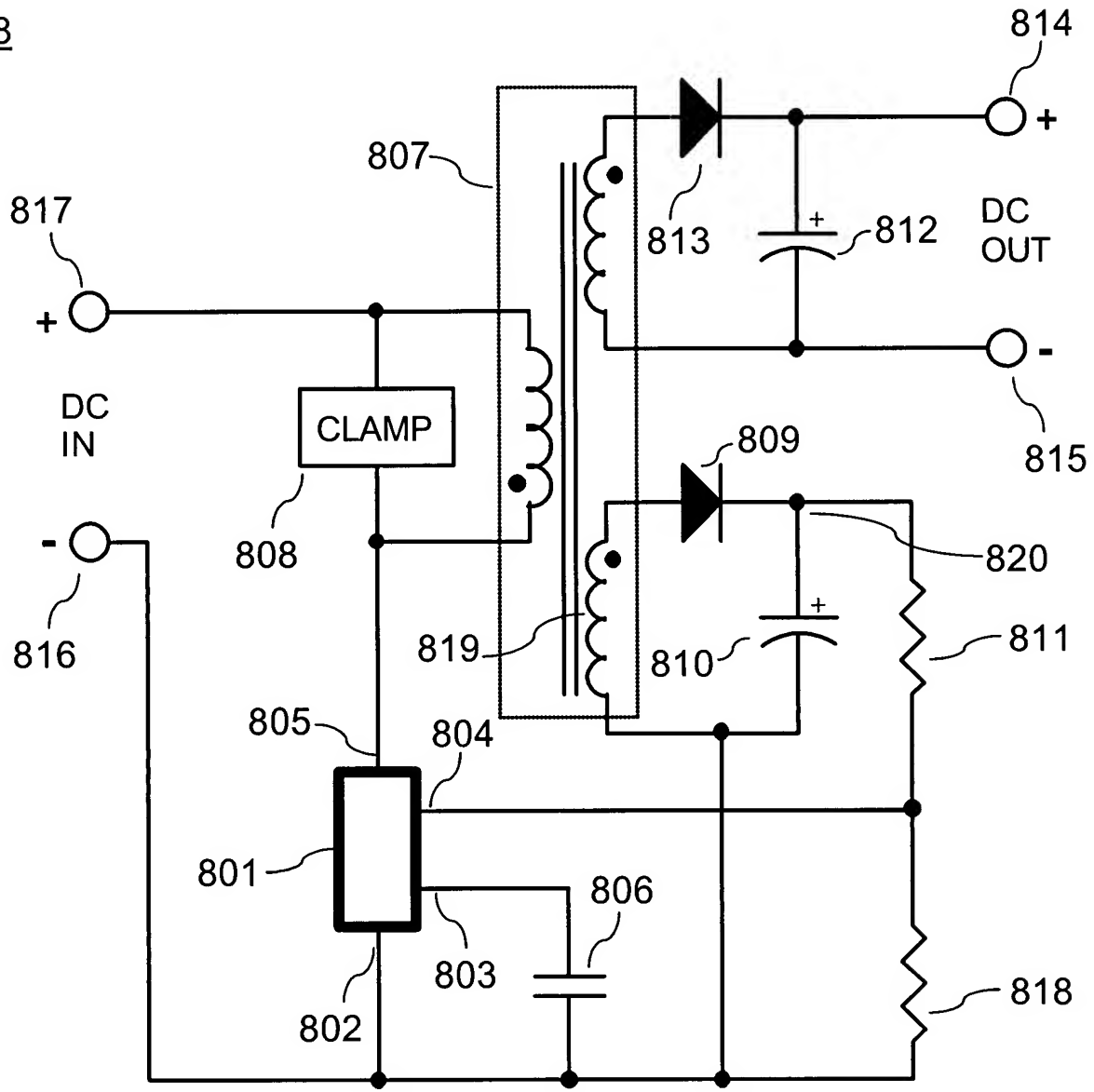
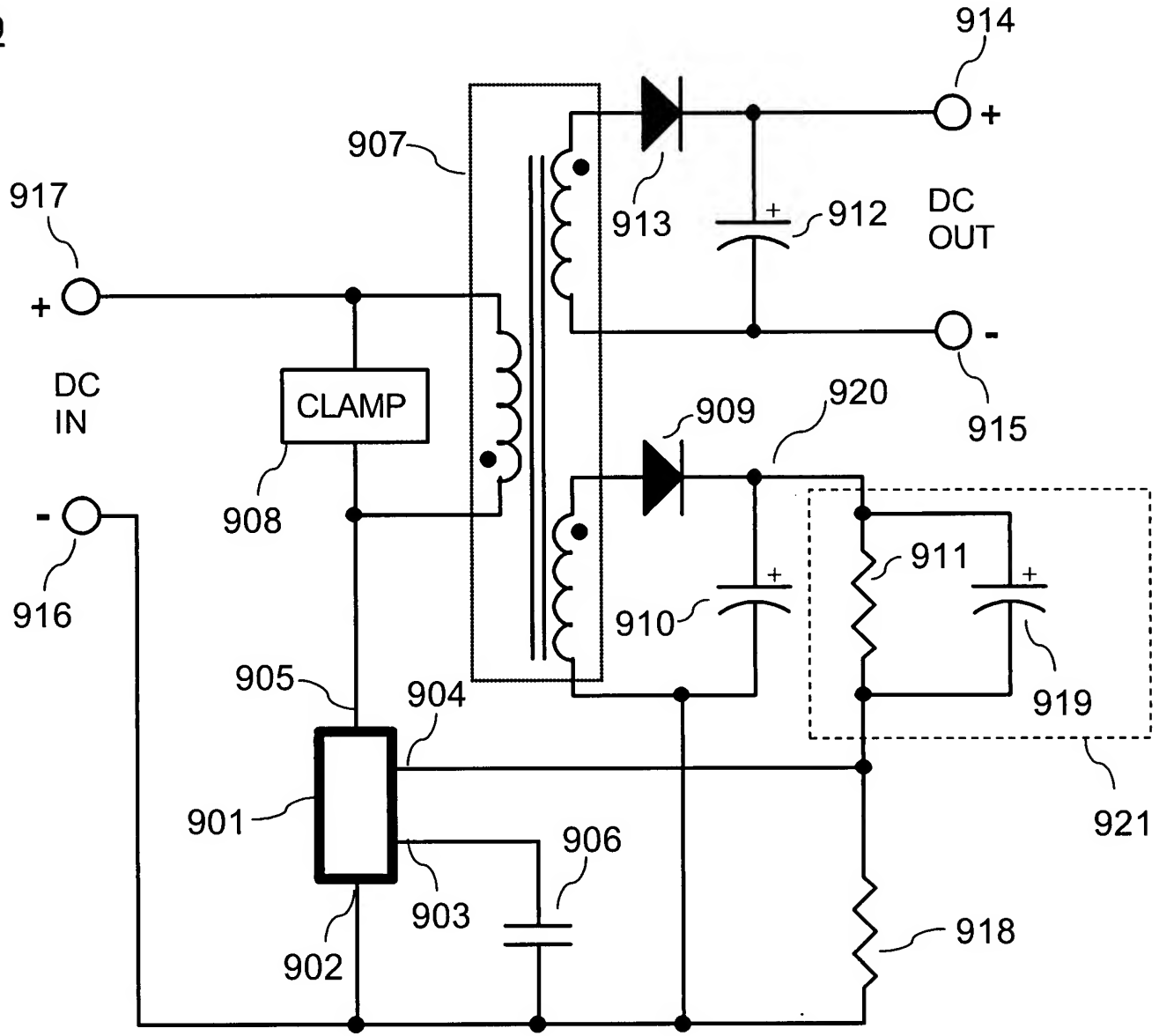

814
+
807
817
813
812
DC
OUT
+
DC
IN
CLAMP
-
815
809
808
820
-
816
819
810
811
805
804
801
806
803
802
818

Figure 9


914
+
907
917
913
912
DC
OUT
+
+
-
DC
IN
CLAMP
909
920
915
908
911
916
910
905
904
919
921
901
906
903
902
918